

ID	Task Name	Start	Finish	Duration	Apr 2010		May 2010			Jun 2010				Jul 2010				Aug 2010				Sep 2010					
					4/11	4/18	5/2	5/9	5/16	5/23	5/30	6/6	6/13	6/20	6/27	7/4	7/11	7/18	7/25	8/1	8/8	8/15	8/22	8/29	9/5	9/12	
1	Verify Lab Performance of design	4/8/2010	4/15/2010	6d																							
2	Order for 5 prototype boards	4/16/2010	4/30/2010	11d																							
3	Build 4 boards in house	5/3/2010	6/3/2010	24d																							
4	RFI Test DTS Module	5/24/2010	5/24/2010	0d																							
5	Install One IF in four antennas	6/4/2010	6/29/2010	18d																							
6	Scientific Validation	6/30/2010	8/10/2010	30d																							
7	Stratix Chips Production Order Procurement Process	4/16/2010	4/16/2010	1d																							
8	Board Production Order Procurement Process	4/19/2010	7/9/2010	60d																							
9	Delivery of Production Stratix Chips	4/19/2010	7/9/2010	60d																							
10	First Article Boards (12)	8/11/2010	10/5/2010	40d																							
11	Production Boards	10/27/2010	12/21/2010	40d																							
12	FPGA Code Written	5/17/2010	10/1/2010	100d																							
13	Install First Article Boards in four antennas	10/6/2010	10/26/2010	15d																							
14	Evaluate First Article	10/6/2010	10/26/2010	15d																							
15	Install 3-bit samplers in Antennas	12/22/2010	4/26/2011	90d																							



HIGH SPEED SAMPLER INSTALLATION ACCELERATED SCHEDULE

WEDNESDAY, APRIL 07, 2010

