

AGENDA
Review to Assess the Design of the Demultiplexer for the
EVLA 3-bit, 4Gsps Digitizer
Tuesday, October 13, 2009
Socorro, NM - DSOC Auditorium

0800 - 0830 Continental breakfast

0830 - 0850 Executive session

0850 - 0900 Welcome - McKinnon

0900 - 0930 Current status of EVLA Project - McKinnon

0930 - 0940 Rationale for selecting current demultiplexer design - McKinnon/Revnell/Durand

0940 - 1030 Digitizer/demultiplexer performance specifications and status/assessment of current demultiplexer design - Revnell/Jackson

1030 - 1050 Break

Alternatives to Current Demultiplexer Design

1050 - 1140 Demux Replacement Options I (Teledyne, Inphi) - Shores

1140 - 1210 Demux Replacement Options II (STm/ALMA) - Durand/Jackson

1210 - 1310 Lunch

1310 - 1400 FPGA Options (Achronix, Altera) - Revnell

1400 - 1410 Summary comparison of alternatives - Revnell/Durand

1410 - 1430 Break

1430 - 1630 Executive session

1630 - 1730 Exit interview

1800 - ?? Dinner at Stage Door Grill